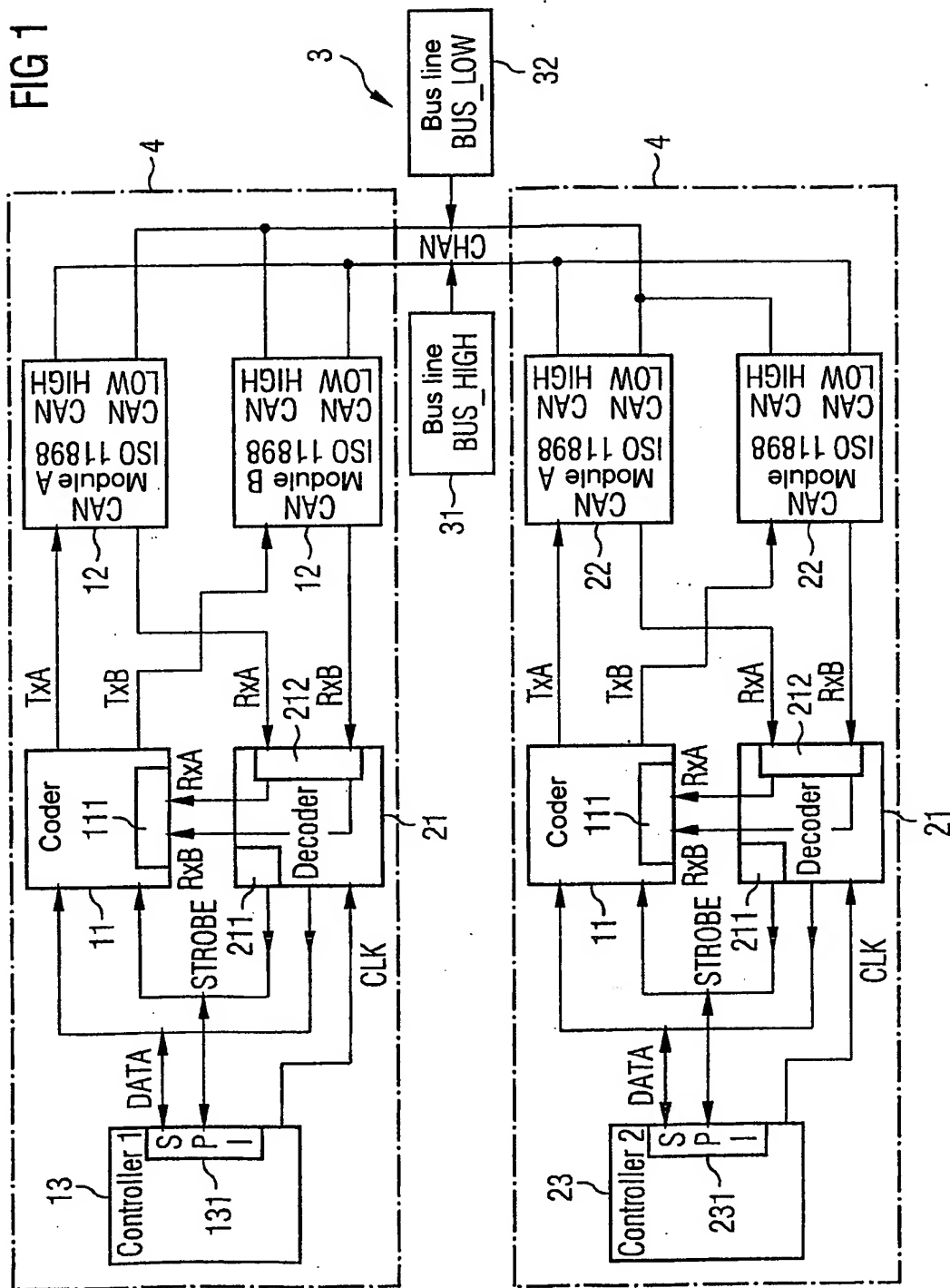


1/10

FIG 1



2/10

FIG 2

Bus	LOW	ZERO	HIGH	BLOCKED
TxA	1	1	0	0
TxB	0	1	1	0
RxA	1	1	0	-
RxB	0	1	1	-

FIG 3

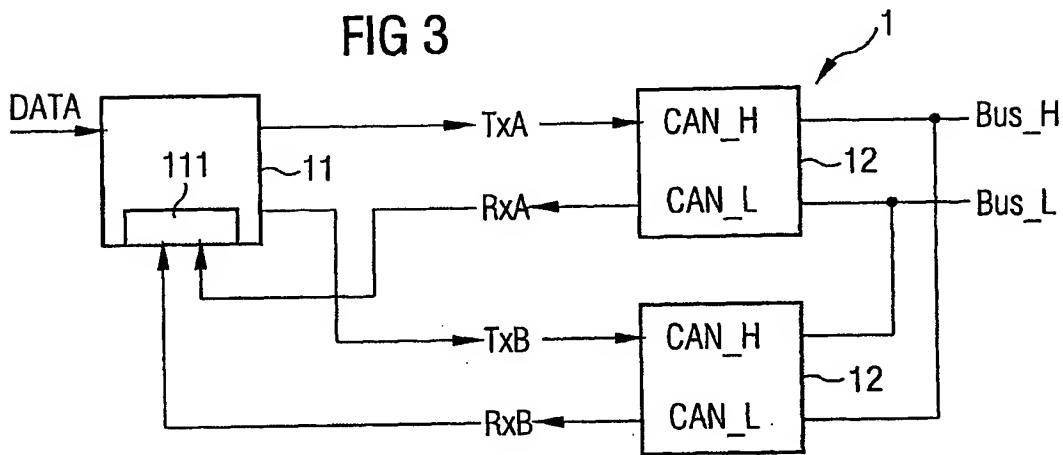
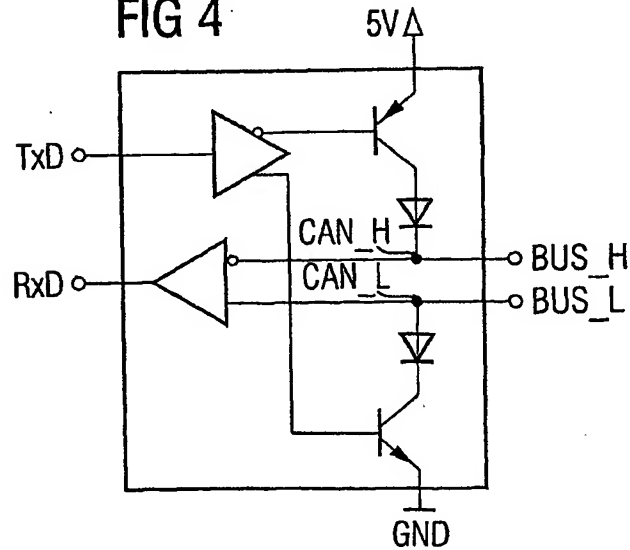
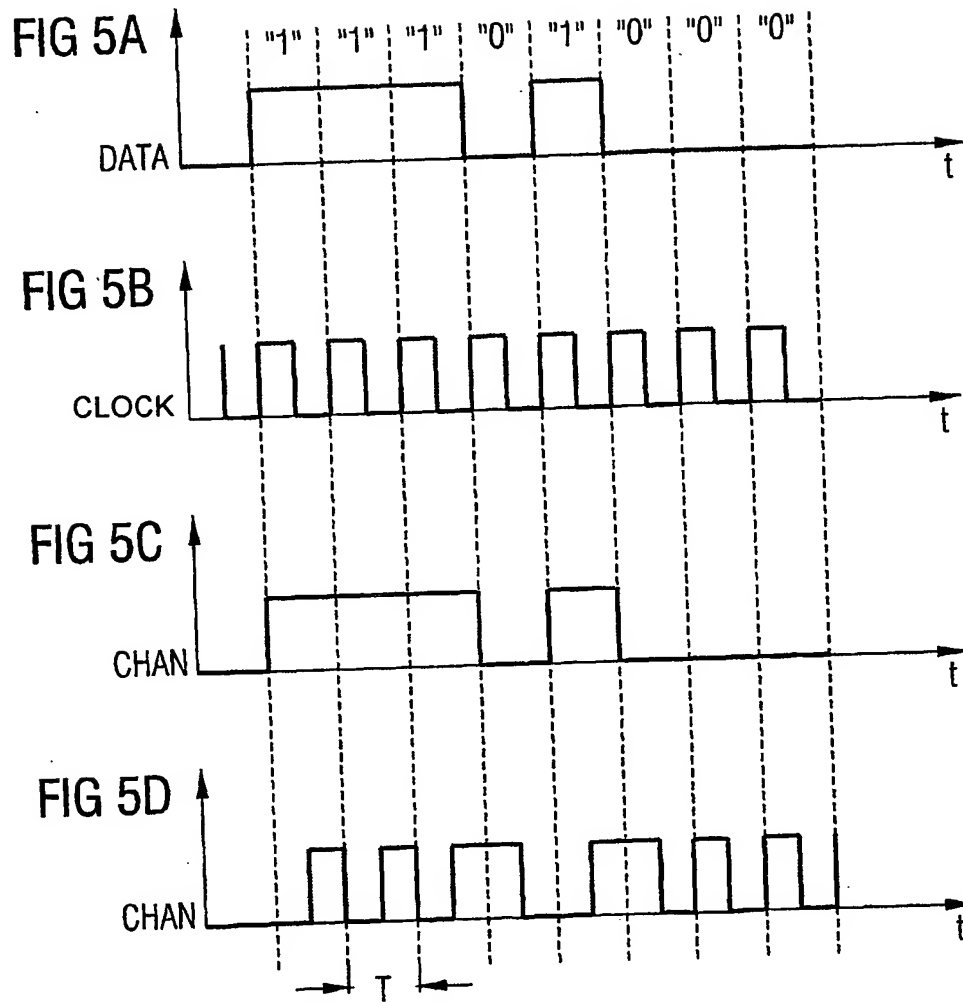


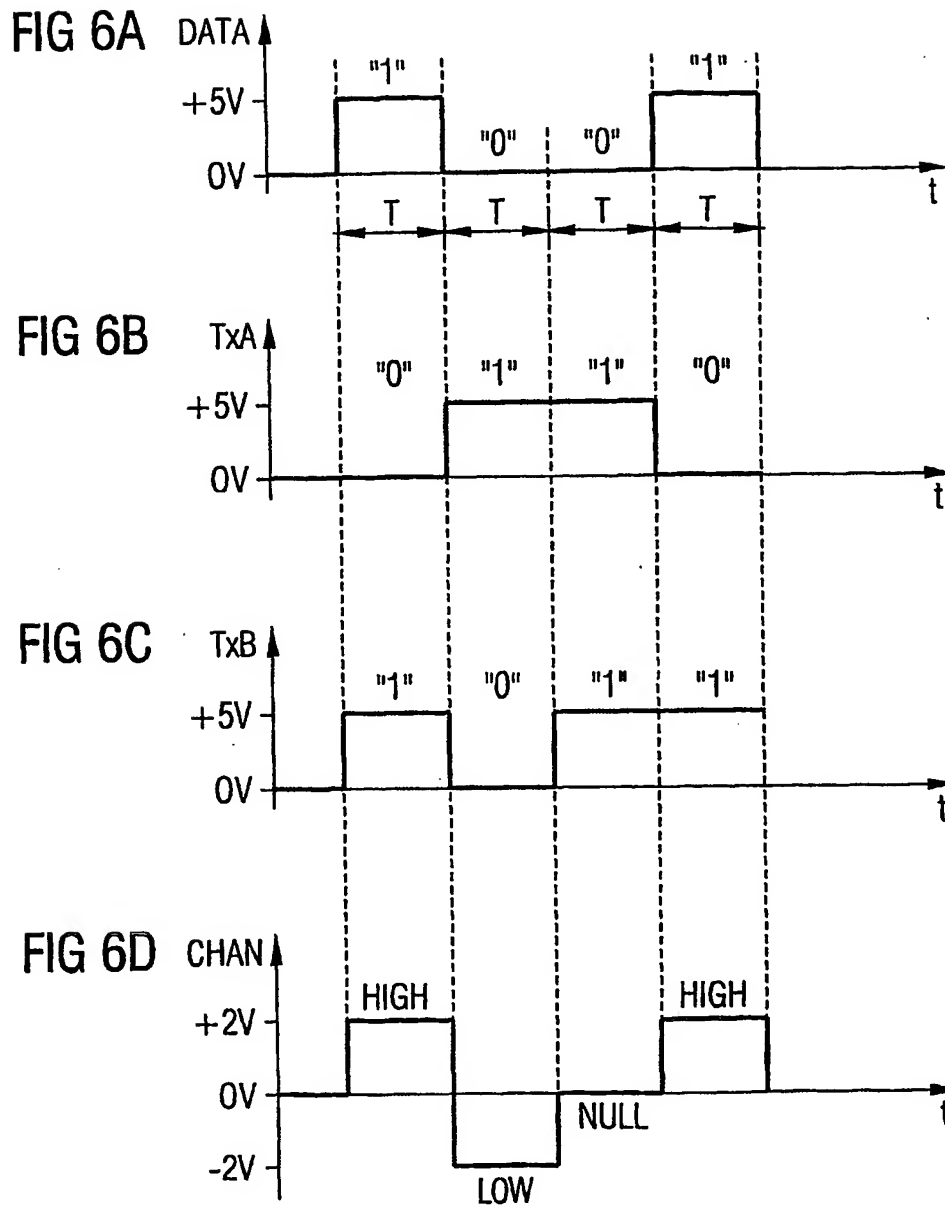
FIG 4



3/10



4/10



5/10

FIG 7

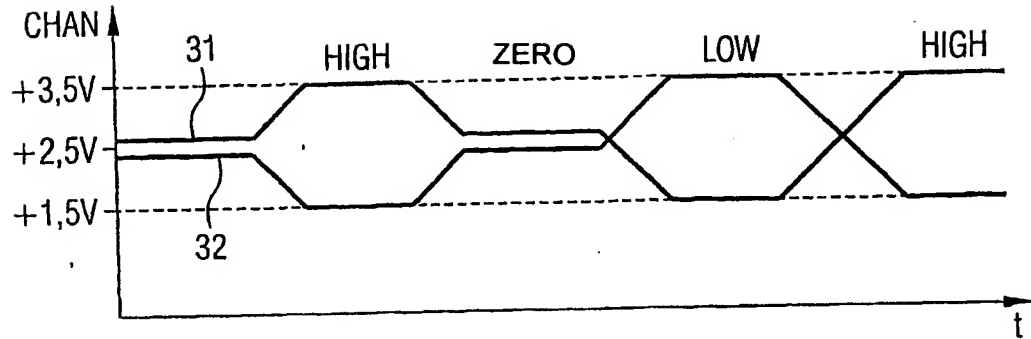


FIG 9A

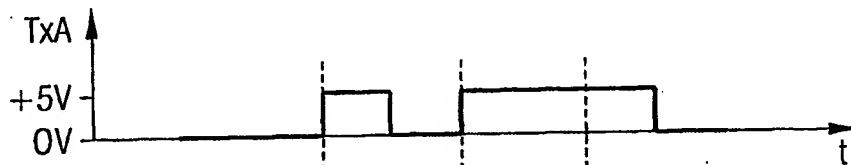


FIG 9B

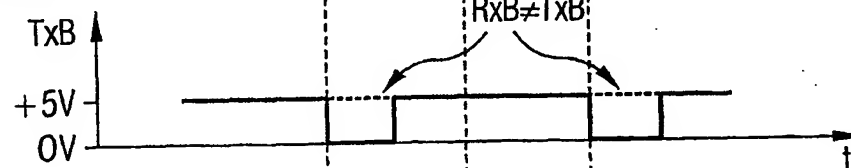
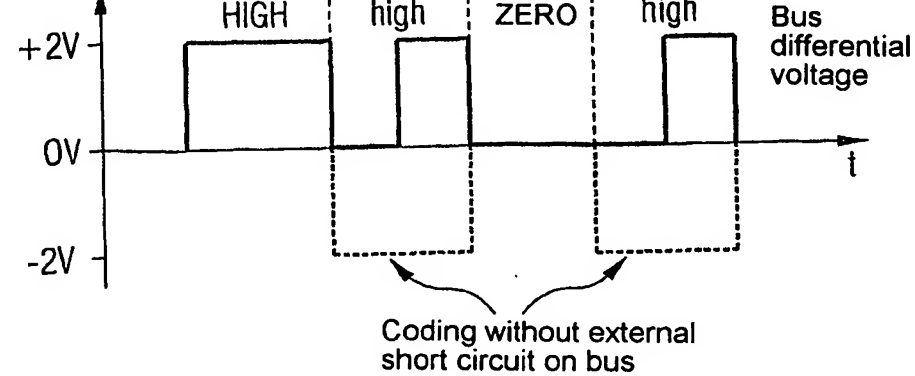


FIG 9C



6/10

FIG 8A Receiver detects:

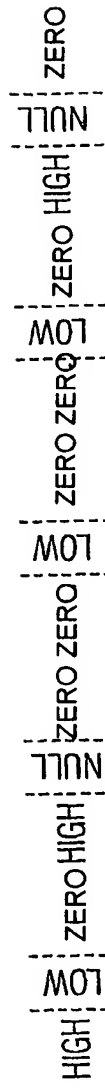


FIG 8B Transmitter sends:

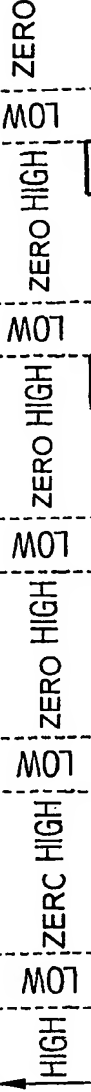


FIG 8C

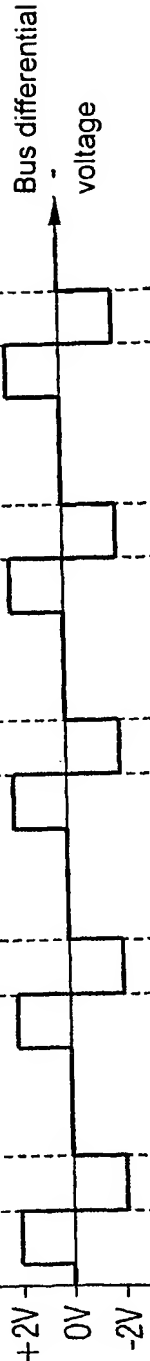


FIG 8D

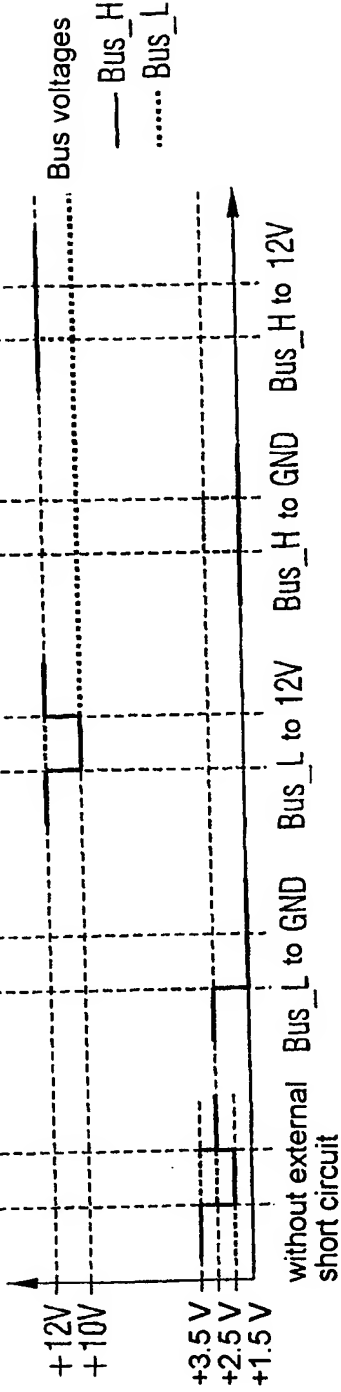


FIG 11

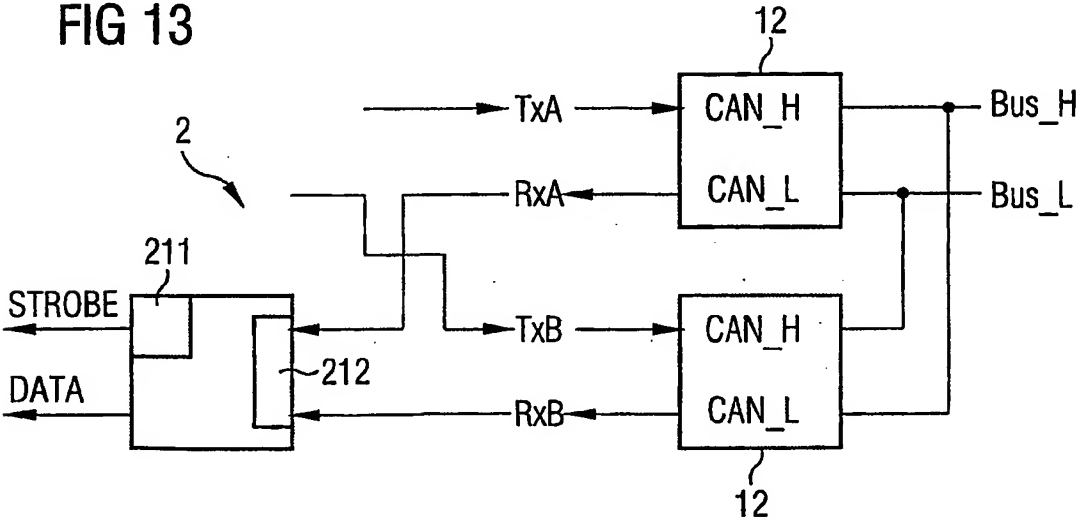
FA and FB=0(RxA and RxB=TxB)

		Tx=1		Tx=0	
Q1 old	Q2 old	Q1	Q2	Q1	Q2
0	1	1	1	1	0
1	1	0	1	1	0
1	0	0	1	1	1

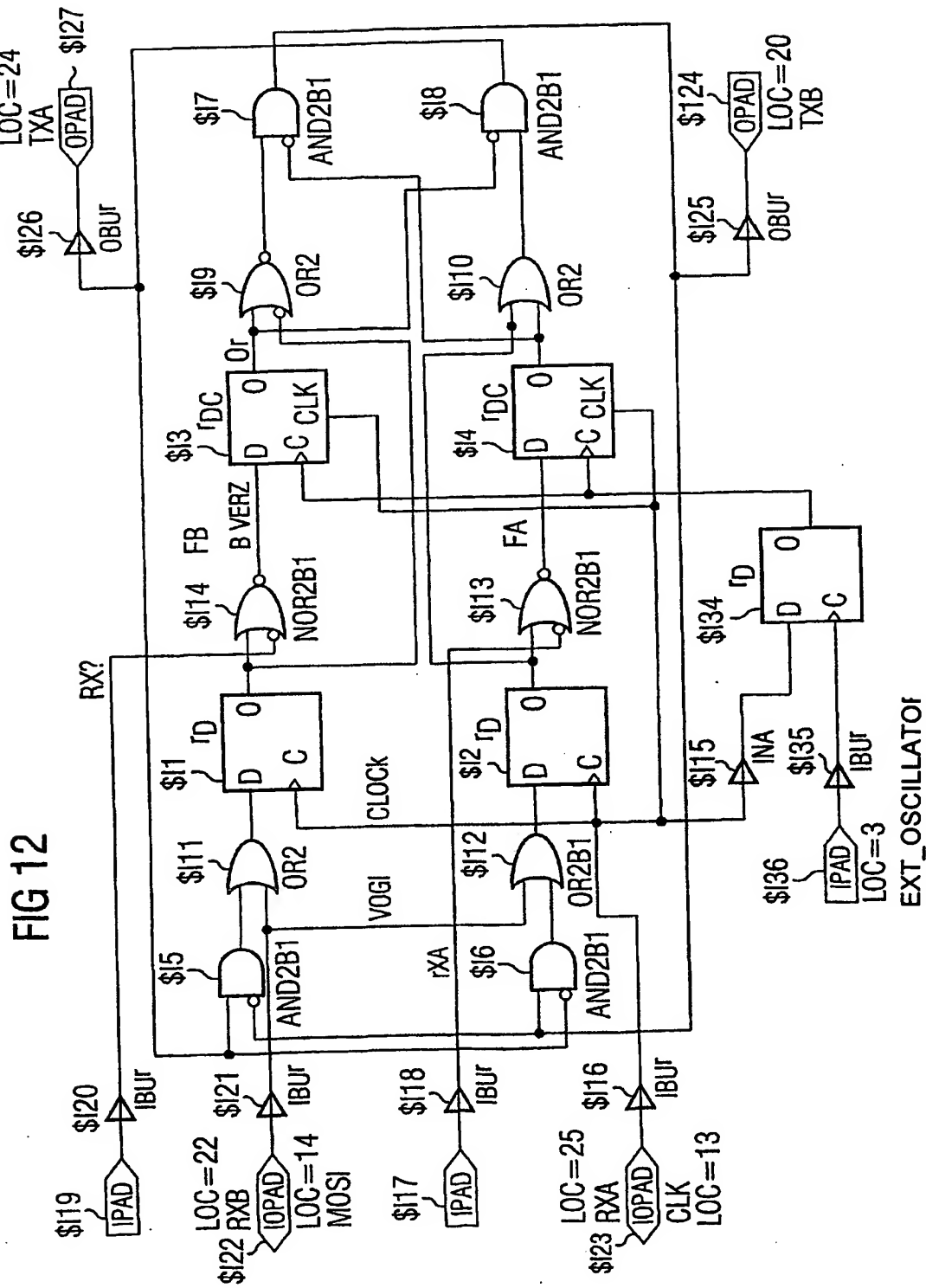
FA and FB=1(RxA≠TxA or RxB≠TxB)

		Tx=1		Tx=0	
Q1 old	Q2 old	Q1	Q2	Q1	Q2
0	1	0	1	1	1
1	1	x	x	x	x
1	0	1	1	1	0

FIG 13



9/10



10/10

FIG 14A

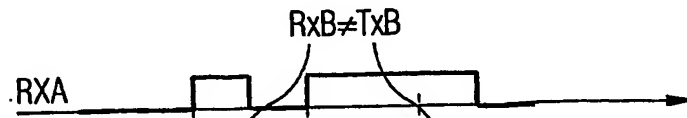


FIG 14B

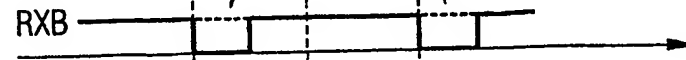


FIG 14C

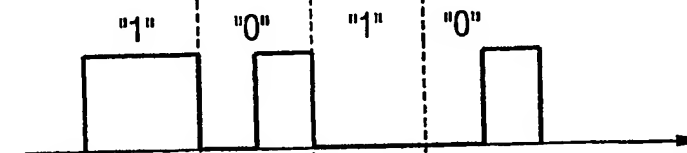


FIG 14D



FIG 14E



FIG 14F



FIG 14G



FIG 15

RxA	11	11	10	01	11	00
RxB	11	11	01	10	00	11
Rx_old	0	1	-	-	-	-
Rx	0	1	0	1	0	1